

Features

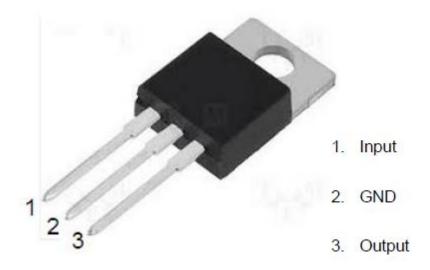
- Output Current of 1.2A
- Thermal Overload Protection
- Short Circuit Protection
- Output transistor safe area protection
- No external components
- Package: TO220
- Output voltage accuracy: tolerance ±5%

General Description

TX78XX is three-terminal positive regulators. One of these regulators can deliver up to 1.5A of output current. The internal limiting and thermal -shutdown features of the regulator make them essentially immune to overload. When used as a

replacement for a zener diode-resistor Combination, an effective improvement in output impedance can be obtained, together with lower quiescent current.

Pin Configuration



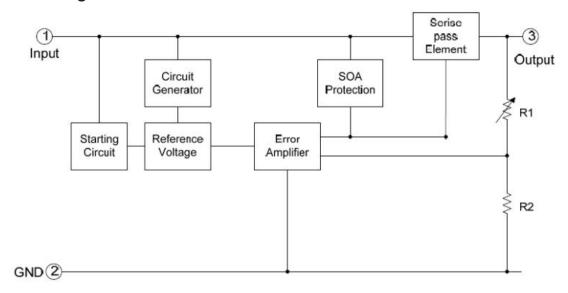


http://www.txsemi.com

Selection Table

Part No.	Output Voltage	Package	Marking
TX7805	5.0V		
TX7806	6.0V		
TX7808	8.0V	TO220	
TX7809	9.0V		
TX7812	12V		

Block Diagram



Absolute Maximum Ratings (Ta=25℃)

Parameter	Rating	Unit
Input supply voltage: VIN	40	V
MAX. Output current:lout	1200	mA
MAX Power:Pmax	1.5	W
Maximum junction temperature:Tj	-25~125	$^{\circ}$
Storage temperature:Tstr	-55~125	°C
Soldering temperature and time	+260(Recommended 10S)	$^{\circ}$

Note: The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.



Electrical Characteristics

(Cin=0.33uF, Co=0.1uF, 0 $\!\!\!<\!\!Tj\!\!\!<\!\!125^{\circ}\!\!\!\mathrm{C}_{\,}$, unless otherwise noted)

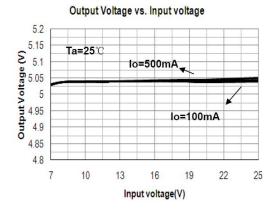
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Output Voltage	Vout	Io=40mA, VIN=10V	0.964vout	vout	1.036vout		
		lo=1mA~40mA	0.96vout	vout	1.04vout	V	
		VIN=7V~18V					
		lo=1mA~10mA	0.05		1.05		
		VIN=10V	0.95vout	vout	1.05vout		
Line Deputation	LND	VIN=7V~18V, Io=40mA	-150	-	150	mV	
Line Regulation	LNR	VIN=8V~18V, Io=40mA	-100	-	100	mv	
	100	VIN=10V, Io=1mA~100mA	-60	-	60	.,	
Load Regulation	LDR	VIN=10V, Io=1mA~40mA	-30	-	30	mV	
Dropout Voltage	V_{DIF}	Tj=25℃,lo=100mA	-	2	-	V	
Output noise	V _N	F=10Hz to 100KHz	-	60	-	uV/Vo	
Voltage						, -	
Ripple Rejection	PSRR	Tj=25℃,f=120Hz,	_	60	-	dB	
Tupple nejection	7 37414	Io=40mA, VIN=8V~20V				45	
Quiescent Current	lα	VIN=10V, IOUT=40mA	-	3	-	mA	
Quiescent Current Change	ΔIQ	VIN=8V~18V, I ₀ =40mA	-1.5	-	1.5	mA	
		VIN=10V,	0.1		0.1		
		IOUT=1mA~40mA,	-0.1	-	0.1		

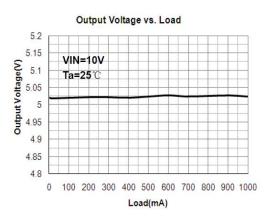
LNR: Line Regulation. The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

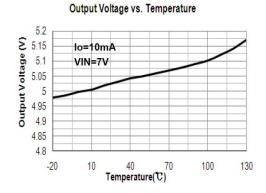
LDR: Load Regulation. The change in output voltage for a change in load current at constant chip temperature.

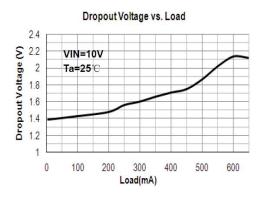
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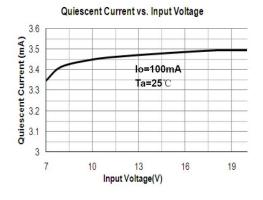
Typical Performance Characteristics

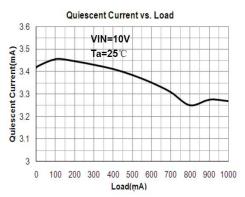












Typical Application

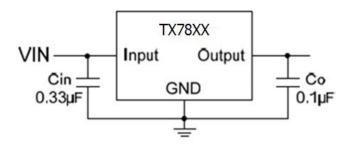
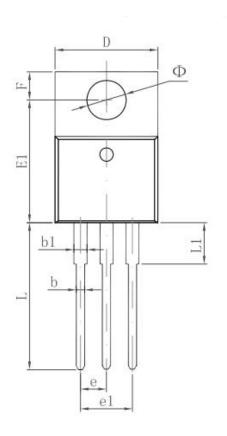
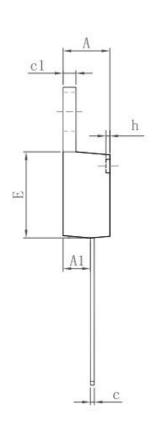


Fig.1 Fixed Output Regulator



Package Information 3-pin TO220 Outline Dimensions





O	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	4.470	4.670	0.176	0.184	
A1	2.520	2.820	0.099	0.111	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	35 0.350	
E1	12.060	12.460	0.475	75 0.491	
е	2.540	TYP	0.100 TYP		
e1	4.980	5.180	0.196 0.2		
F	2.590	2.890	0.102	0.114	
h	0.000	0.300	0.000	0.012	
L	13.400	13.800	0.528	8 0.543	
L1	3.560	3.960	0.140	0.156	
Φ	3.735	3.935	0.147	0.155	



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